

## TUESDAY

8:00 AM	10	Dougal	USC	Introduction
8:10 AM	20	Tucker	ONR	Welcome
8:30 AM	20	Hingorani	HPE	Plug and Play
8:50 AM	60	Ericson	ONR	Plug and Play Vision
9:50 AM	20	<b>AM Break</b>		
10:10 AM	<b>Virtual Building Blocks</b>			
10:10 AM	20	Dougal	USC	Virtual Building Blocks - the VTB
10:30 AM	50	Cokkinides	USC	VTB demonstrations
11:20 AM	30	Hudgins	USC	Power device tradeoffs
11:50 AM	10		USC	VTB User Information
12:00 PM	60	<b>LUNCH</b>		
1:00 PM	<b>Connecting the Building Blocks</b>			
1:00 PM	30	Hartzell	Satcon	Coordinated Interconnect Demonstrator
1:30 PM	30	Harris	Mandign	The Coordinated Interconnect System
2:00 PM	30	Borojevic	VT CPES	Modularized PEBB Concept and PS Arch for Flexible System Integration
2:30 PM	30	Kelley	NCSU	Bus connector for coordinated interconnect: Measurement and finite element simulation
3:00 PM	15	<b>PM Break</b>		
3:15 PM	<b>Commercializing Building Blocks</b>			
3:15 PM	30	Selvidge	Vector	PEBB Commercialization
3:45 PM	30	Donegan	SPCO	PEBB Technology Commercialization at SPCO
4:15 PM	60	<b>Plug &amp; Play Breakout</b> -- Planning sessions for PCIM 99		
5:15 PM	30	<b>Return to Hotel</b>		

## WEDNESDAY

8:00 AM	<b>PEBB Transition to SPCO</b>			
8:00 AM	15	Mehta	SPCO	SPCO Embraces PEBB concept over wide range of power
8:15 AM	50	Temple	SPCO	Status of SPCO/Harris PEBB Technology
9:05 AM	30	Arthur	SPCO	PEBB Transition to SPCO
9:35 AM	15	<b>AM Break</b>		
9:50 AM	<b>PEBB Applications</b>			
9:50 AM	20	Singh	ARL	PEBB Applications in DOD Pulsed Systems
10:10 AM	45	Borraccini	NSWC	NSWC PEBB testbed
10:55 AM	30	Duong	Northrop	New soft switching topology
11:25 AM	30	Bose	U Tenn/K	Converter and Control Sys Design for MCT-based Motor Drive App Facility
11:55 AM	30	Ykema	SPD	PEBB application in Power Node Control Center
12:25 PM	60	<b>Lunch</b>		
1:25 PM	<b>Devices for the Power Block</b>			
1:25 PM	15	Chu	Sandia	MCT's as ideal switch for high di/dt applications
1:40 PM	15	You	VT CPES	MCT/IGBT option reduced to practice
1:55 PM	30	Chow/Huang	RPI/VPI	Advanced Power Semi Devices for PEBB
2:25 PM	30	Driscoll	PTS	Dual gated Thyristors
2:55 PM	30	Kub	NRL	Double-side IGBT by Low Temp Wafer Bonding
3:25 PM	15	<b>PM Break</b>		
3:40 PM	30	Kornegay		Submicron CMOS for SmartPower Apps
4:10 PM	30	Lipo	Wisc	MTO Switching Behavior in a soft-switched converter
4:40 PM	20	Shenai	UIC	High-power IGBT Module for PEBB Apps

5:00 PM      **Return to Hotel**

## **THURSDAY**

### **Thermal Management**

8:00 AM	20 Lu	VT CPES	PEBB Packaging Research at Va Tech
8:20 AM	30 Joshi	U Md	Design/Mod of flow-through AlSiC Baseplate
8:50 AM	30 Jacobsen	SPAWAR	Metal Matrix Composites for Power Modules
9:20 AM	30 Harrison	Vector	PEBB Thermal Management
9:50 AM	15 AM Break		

### **Reliability**

10:05 AM	20 Azarm	U Md	Decision support framework for PEBB packaging design
10:25 AM	20 Shenai	UIC	Proposed Framework for PEBB Reliability Engineering
10:45 AM	90	<b>Review/Discussion of Plans for PCIM 99</b>	
12:15 PM		<b>Adjourn Meeting *</b>	

\* Sandwich lunches will be available for those who wish to stay to complete planning into the early afternoon.